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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/540,410	06/24/2005	Carl Glasse	GB02 0256 US	4245

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EXAMINER

LE, DUNG ANH

ART UNIT	PAPER NUMBER
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2818

DATE MAILED: 11/03/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/540,410

Applicant(s)

GLASSE ET AL.

Examiner

DUNG A. LE

Art Unit

2818

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 02 October 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-13 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-4, 9-11 and 13 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 24 June 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☒ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 6/24/2005.

- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Priority

Acknowledge is made of applicants' claim for foreign priority base on an application 0230140.6 filed in United Kingdom on 12/24/2002.

Oath/Declaration

The oath/declaration filed on 6/24/2005 is acceptable.

Election/Restriction

Application's election without traverse of Group I (Claims 1-4,9-11 and 13) drawn to a semiconductor is acknowledged for prosecution in the subject application .

Applicants have the right to file a divisional, continuation or continuation-in-part application covering the subject matter of the non-elected claims.

Information Disclosure Statement

This office acknowledges of the following items from the Applicant:

Information Disclosure Statement (IDS) filed on 6/24/2005 has/have been considered and made of record. The references cited on the PTOL 1449 form have been considered.

Specification

The specification has been checked to the extent necessary to determine the presence of all possible minor errors. However, the applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Claim Rejections

Claim Rejections - 35 USC § 112

Claim 13 is rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. In claim 13, “an electronic device substantially as described herein with reference to the drawings”, is not clear for examination.

The remaining claims are dependent from the above rejected claims and therefore also considered indefinite.

Set of claims 1-4, 9-11

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1- 4 and 11 are rejected under 35 USC 102 (b) as being anticipated by Yoshikawa (4925807).

Yoshikawa teaches thin film transistor on a substrate comprising: a semiconductor layer having a first doped region 27b and a second doped region 27a in between a first further doped region 28b and a second further doped region 28a , and having an undoped region in between the first doped region and the second doped region (particularly figs. 2b, 4b and 5b and related texts) , the first doped region and the second doped region having a lower conductivity than the first further doped region and the second further doped region; and an oxide layer 22 partially covering a surface of the semiconductor layer, the oxide layer carrying:

a conductive gate 23 over the undoped region having a first side and a second side substantially perpendicular to the oxide layer; a first spacer 24 and a second spacer adjacent to the first side and second side of the conductive gate respectively; a first insulating spacer 29 adjacent to a side of the first spacer opposite the first side of the conductive gate; and a second insulating spacer adjacent to a side of the second spacer opposite the second side of the conductive gate; the thin film transistor further comprising: a first

conductive contact 30/30b with the first further doped region 28a ; and a second conductive contact with the second further doped region 28b.

Regarding claim 2, wherein the first spacer 25 and the second spacer comprise a conductive material (col. 3, line(s) 15-20) .

Regarding claim 3, wherein the first conductive contact and the second conductive contact comprise a silicide layer 30b (col 5, lines 25-30).

Regarding claim 4, wherein the semiconductor layer 21 comprises a polycrystalline silicon material.

Regarding claims 11, thin film transistor substantially as described herein with reference to the drawings (particularly figs. 2b, 4b and 5b and related texts) .

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 9- 10 are rejected under 35 U.S.C. 103 (a) as being unpatentable over Yoshikawa in view of the following remark.

Yoshikawa teaches the claimed invention as applied to claim 1 except for an electronic device comprising an active matrix array coupled to a first driver circuit arrangement and a second driver circuit arrangement, the first driver circuit arrangement and the second driver circuit arrangement being coupled to a power supply, at least one of the matrix array, the first driver circuit arrangement and the second driver circuit arrangement comprising a plurality of claimed thin film transistors and wherein the power supply comprises battery means as cited in current claims 9-10.

It would have been obvious to one having ordinary skill in the art at the time the invention was made to form an electronic device comprising an active matrix array coupled to a first driver circuit arrangement and a second driver circuit arrangement, the first driver circuit arrangement and the second driver circuit arrangement being coupled to a power supply, at least one of the matrix array, the first driver circuit arrangement and the second driver circuit arrangement comprising a plurality of claimed thin film transistors and wherein the power supply comprises battery means, the above mentioned limitation is commonly used to greatly enhance the efficiency of the claimed thin film transistor and its superior electronic characteristic can be achieved, since it has been held

to be within the general skill of a worker in the art to select a known material on the basis of its suitability for the particular practice.

Regarding Independent claim 13, an electronic device substantially as described herein with reference to the drawings (figs. 2b, 4b and 5b and related texts).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dung A. Le whose telephone number is (571) 272-1784. The examiner can normally be reached on Monday-Tuesday and Thursday 6:00am- 4:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, M. Smith can be reached on (571) 272-1907. The central fax phone numbers for the organization where this application or proceeding is assigned are (571)272-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DUNG A. LE
Primary Examiner
Art Unit 2818

